

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In Re Application of:)	
)	
J. Brask et al.)	
)	
U.S. Serial No: 10/748,559)	Examiner: T. Le
)	
Filed: December 29, 2003)	Art Unit: 2814
)	
For: A CMOS DEVICE WITH)	
METAL AND SILICIDE GATE)	
ELECTRODES AND A)	
METHOD FOR MAKING IT)	
)	

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

AMENDMENT AND RESPONSE

Dear Sir:

This is in response to the Office Action mailed June 6, 2006. Applicant respectfully requests the Examiner to consider the following remarks. No claims are being amended.

Remarks/Arguments begin on page 2 of this paper.